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**DECLARATION FOR UTILITY OR
DESIGN
PATENT APPLICATION
(37 CFR 1.63)**

Declaration
Submitted
with Initial
Filing

OR

Declaration
Submitted after Initial
Filing (surcharge
(37 CFR 1.16 (e))
required)

Attorney Docket Number DB001062-000

First Named Inventor

Mark Beaumont

COMPLETE IF KNOWN

Application Number

Filing Date

20 October 2003

Art Unit

Examiner Name

As the below named inventor, I hereby declare that:

My residence, mailing address, and citizenship are as stated below next to my name.

I believe I am the original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

**METHOD FOR LOAD BALANCING AN N-DIMENSIONAL ARRAY OF PARALLEL
PROCESSING ELEMENTS**

(Title of the Invention)

the specification of which

 is attached hereto

OR

 was filed on (MM/DD/YYYY)

as United States Application Number or PCT International

Application Number and was amended on (MM/DD/YYYY) (if applicable).

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment specifically referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

I hereby claim foreign priority benefits under 35 U.S.C. 119(a)-(d) or (f), or 365(b) of any foreign application(s) for patent, inventor's or plant breeder's rights certificate(s), or 365(a) of any PCT international application which designated at least one country other than the United States of America, listed below and have also identified below, by checking the box, any foreign application for patent, inventor's or plant breeder's rights certificate(s), or any PCT international application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application Number(s)	Country	Foreign Filing Date (MM/DD/YYYY)	Priority Not Claimed	YES	NO
0309204.6	UK	04/23/2003	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	<input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>	<input checked="" type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/> <input type="checkbox"/>

 Additional foreign application numbers are listed on a supplemental priority data sheet PTO/SB/02B attached hereto:

[Page 1 of 2]

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DECLARATION — Utility or Design Patent Application

Direct all correspondence to:

Customer Number
or Bar Code Label

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Correspondence address below

24122

PATENT TRADEMARK OFFICE

Name

Address

City

State

ZIP

Country

Telephone

Fax

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C. 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

NAME OF SOLE OR FIRST INVENTOR : A petition has been filed for this unsigned inventor

Mark

Given Name
(first and middle [if any])

Beaumont

Family Name
or SurnameInventor's
Signature*J. M. Beaumont*

Date 6-AUG-2003

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NAME OF SECOND INVENTOR: A petition has been filed for this unsigned inventorGiven Name
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or SurnameInventor's
Signature

Date

Residence: City

State

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ZIP

Country

 Additional inventors are being named on the _____ supplemental Additional Inventor(s) sheet(s) PTO/SB/02A attached hereto.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
37 C.F.R. § 3.73(b) Statement

Applicants	Title	Serial No.	Filing Date
Mark Beaumont	METHOD FOR LOAD BALANCING AN N- DIMENSIONAL ARRAY OF PARALLEL PROCESSING ELEMENTS		

Micron Technology, Inc. is the assignee of the entire right, title and interest in the patent applications identified above by virtue of either:

A. An assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the United States Patent and Trademark office on _____ at Reel _____ Frame _____, or for which a copy thereof is attached.

OR

B. A chain of title from the inventor(s) of the patent application identified above to the current assignee as shown below:

1. From _____ To: _____
The document was recorded in the United States Patent and Trademark Office on _____ at Reel _____, Frame _____, or for which a copy thereof is attached.

The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the assignee.

Power of Attorney

I hereby revoke all previous powers of attorney, if any, and appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith:

Edward L. Pencoske Reg. No. 29,688
Richard J. Coldren Reg. No. 44,084

Michael L. Lynch Reg. No. 30,781
Jenifer S. Tarasi Reg. No. 46,064
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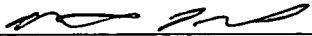
Declaration

I hereby declare that my presentation of this paper constitutes a certification under 37 C.F.R. § 10.18, which provides, in part, that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and that further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful statements may jeopardize the validity of the application and any patent issuing therefrom.

Date

Oct 13, 2003

Signature



Name

Michael L. Lynch

Title

Chief Patent Counsel